

FIG. 1A

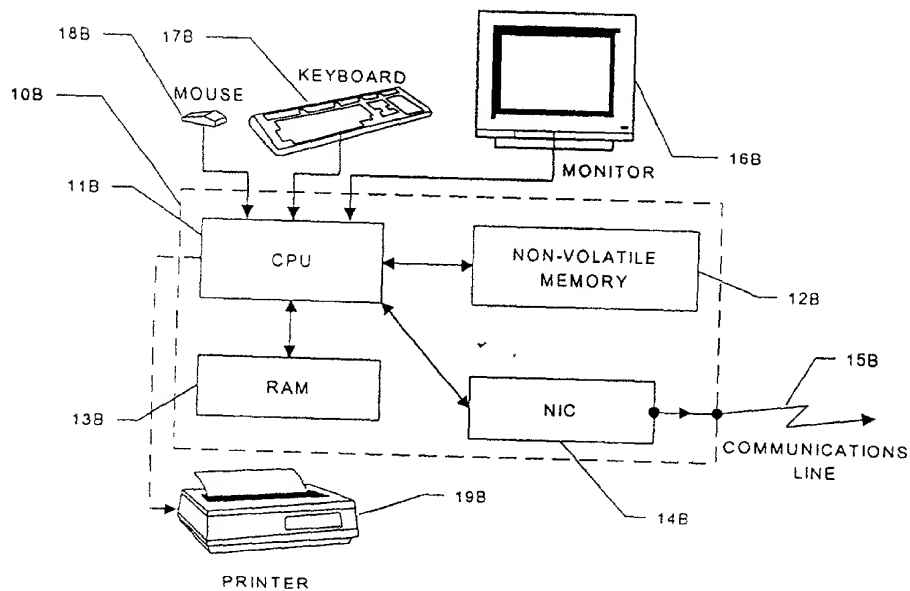
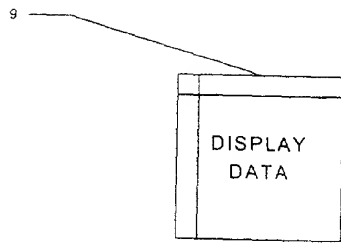


FIG. 1B



Insert memory mappings for volatile and non-volatile memory.

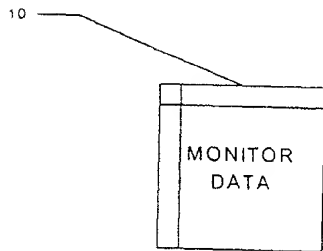


FIG. 1C

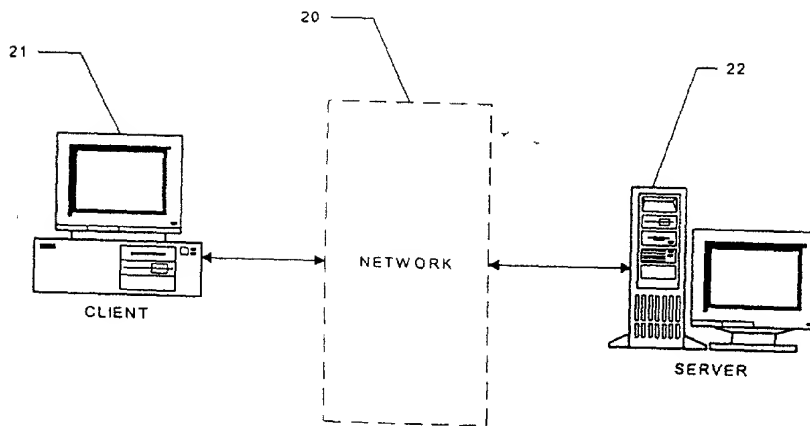


FIG. 1D

09861577 061401
FOI b6 b7C

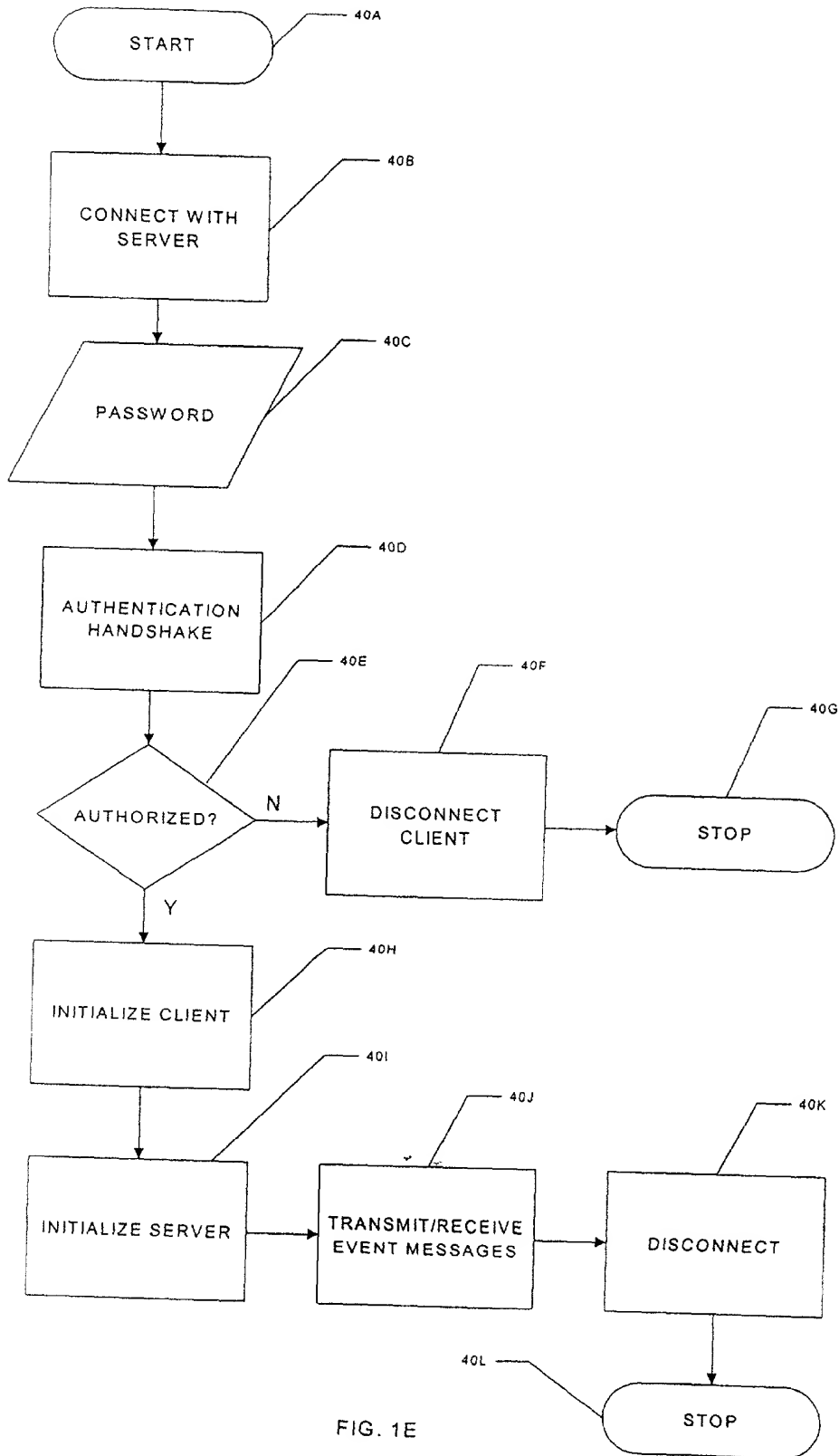


FIG. 1E